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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hideomi Suzawa et al.

Art Unit : 2812

Serial No.: 09/852,672 Filed

: May 11, 2001

Examiner: Viktor Simkovic

Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

A copy of the reference listed on the attached form PTO-1449 is enclosed.

This statement is being filed after a first Office action on the merits, but before receipt of a final Office action or a Notice of Allowance. A check for \$180 in payment of the late submission fee of §1.17(p) is enclosed. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: May 6, 2003

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| Substitute Form PTO-1449 (Modified) | U.S. Department of Commerce Patent and Trademark Office | Attorney's Docket No. 12732-038001 | Application No. 09/852,672 |
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| Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b)) | | Applicant Hideomi Suzawa et a | ıl. |
| | | Filing Date May 11, 2001 | Group Art Unit 2812 |

| [| | | | U.S. Pater | nt Documents | | | |
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| | Foreign Patent Documents or Published Foreign Patent Applications | | | | | | | |
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| Initial | ID | Number | Date | Patent Office | Class | Subclass | Yes | No |
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Substitute Disclosure Form (PTO-1449)